

IQXO-62, -63

ISSUE 4 ; 19 OCTOBER 2004

Delivery Options

- Please contact our sales office for current leadtimes

Output Compatibility

- Tri-state HCMOS/TTL (5.0V) (IQXO-62)
- Tri-state HCMOS (3.3V) (IQXO-63)

Package Outline

- SMD Ceramic Package

Standard Frequency Stabilities

- $\pm 50\text{ppm}$, $\pm 100\text{ppm}$ (inclusive of supply voltage & output load)

Operating Temperature Ranges

- -10 to 70°C (IQXO-62, 63)
- -40 to 85°C (IQXO-62I, -63I)

Storage Temperature Range

- -40 to 85°C

Tri-state Operation

- Logic '1' to pad 1 enables oscillator output, IQXO-62 2.2V min, IQXO-63 0.7Vs min
- Logic '0' to pad 1 disables oscillator output; when the oscillator output goes to the high impedance state, IQXO-62 0.8V max, IQXO-63 0.3Vs max
- No connection to pad 1 enables oscillator output

Solder Conditions

- For typical soldering conditions, please see the relevant pages in Applications Notes

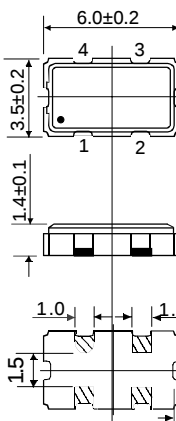
Marking

- Model number
- Frequency Stability Code
- Frequency

Minimum Order Information Required

- Frequency + Model Number + Frequency Stability

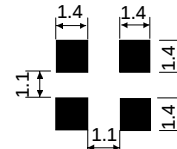
Outline in mm



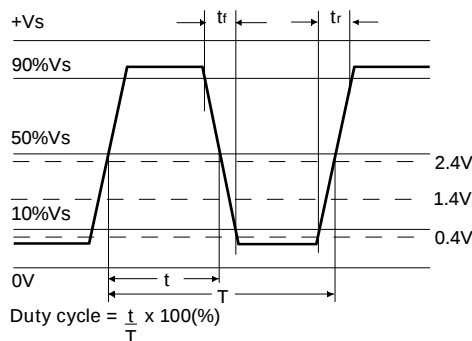
Pad Connections

1. Enable/Disable
2. GND
3. Output
4. +Vs

Solder pad layout



Output Waveform - HCMOS/TTL



Electrical Specification - maximum limiting values when measured in HCMOS test circuit

Frequency Range	Frequency Stability	Supply Voltage	Supply Current	Rise Time(tr)	Fall Time (tf)	Duty Cycle	Model Number
1.8 to 25.0MHz	$\pm 50\text{ppm}$, $\pm 100\text{ppm}$	3.3V $\pm 0.3\text{V}$	12mA	7ns	7ns	40/60%	IQX0-63
		5.0V $\pm 0.5\text{V}$	27mA	7ns	7ns		IQX0-62
> 25.0 to 50.0MHz	$\pm 50\text{ppm}$, $\pm 100\text{ppm}$	3.3V $\pm 0.3\text{V}$	16.5mA	7ns	7ns	40/60%	IQX0-63
		5.0V $\pm 0.5\text{V}$	45mA	7ns	7ns		IQX0-62

Ordering Example

Frequency 24.0MHz IQX0-62 I B
Model No _____
Operating Temperature Code; I = -40 to 85°C; Not applicable for 0 to 70°C _____
Frequency Stability: B = $\pm 50\text{ppm}$; C = $\pm 100\text{ppm}$ _____

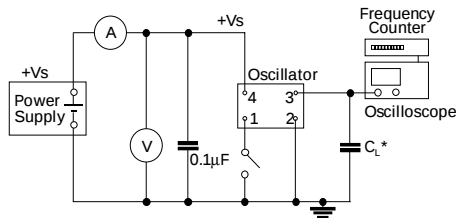
Note. Only code IC is available

Please note that the rise and fall times listed are the maximum values we specify to cover various frequency breaks. In practise the actual values are generally lower depending upon the spot frequency chosen. For typical values please contact our sales office.

Note. Other frequency / specification combinations may be available so please contact our sales office.

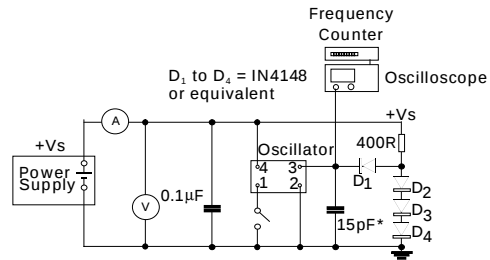
SURFACE MOUNT
SPX08

Test Circuit - HCMOS



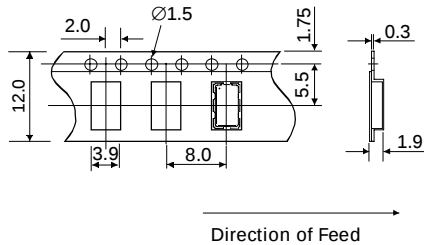
* Inclusive of jigging & equipment capacitance
Note: $C_L = 50\text{pF}$ for model IQX0-62 and 15pF for model IQX0-63

Test Circuit - TTL (IQX0-62)



* Inclusive of jigging & equipment capacitance

Outline in mm - Tape



Outline in mm - Reel

