

PROTECTION PRODUCTS - RailClamp®

Description

RailClamp® low capacitance TVS array is designed to protect high speed data interfaces. This series has been specifically designed to protect sensitive components which are connected to data and transmission lines from overvoltage caused by **ESD** (electrostatic discharge), **CDE** (Cable Discharge Events), and **Lightning**.

The unique design incorporates surge rated, low capacitance steering diodes and a TVS diode in a single package. During transient conditions, the steering diodes direct the transient current to ground. The internal TVS diode clamps the transient voltage to a safe level. The low capacitance array configuration allows the user to protect up to four high-speed data lines.

The RClamp®3304NA is constructed using Semtech's proprietary EPD process technology. The EPD process provides low stand-off voltages with significant reductions in leakage current and capacitance over silicon-avalanche diode processes. The RClamp3304NA features a true operating voltage of 3.3 volts for superior protection.

This device is in a 10-pin SLP2626P10 package. It measures 2.6 x 2.6 x 0.60mm. The leads are spaced at a pitch of 0.5mm and are finished with lead-free NiPdAu. The high surge capability ($I_{pp}=25A$, $t_p=8/20\mu s$) means it can be used in high threat environments in applications such as Gigabit Ethernet, telecommunication lines, and digital video.

Features

- ◆ Transient protection for high-speed data lines to **IEC 61000-4-2 (ESD) 25kV (air), 15kV (contact)**
IEC 61000-4-4 (EFT) 40A (5/50ns)
IEC 61000-4-5 (Lightning) 25A (8/20μs)
- ◆ Array of surge rated diodes with internal TVS Diode
- ◆ Small package saves board space
- ◆ Protects up to four I/O lines
- ◆ Low capacitance (**<5pF**) for high-speed interfaces
- ◆ Low leakage current and clamping voltage
- ◆ Low operating voltage: **3.3V**
- ◆ Solid-state silicon-avalanche technology

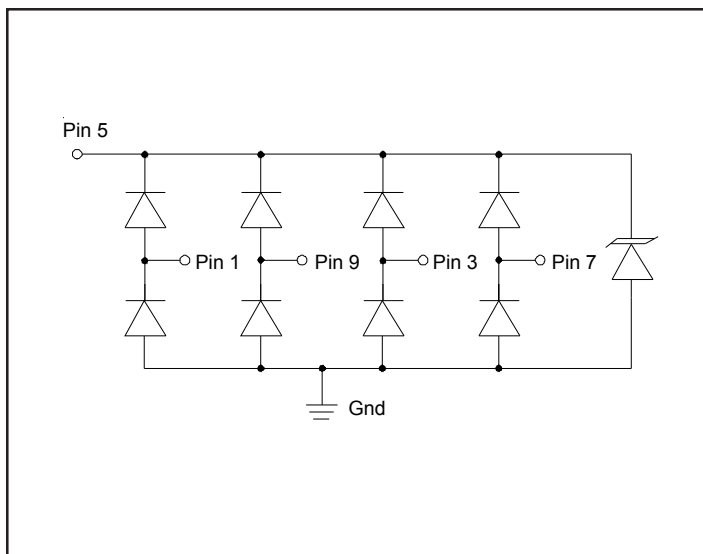
Mechanical Characteristics

- ◆ SLP2626P10 10L package
- ◆ Pb-Free, Halogen Free, RoHS/WEEE Compliant
- ◆ Nominal Dimensions: 2.6 x 2.6 x 0.60 mm
- ◆ Lead Finish: NiPdAu
- ◆ Molding compound flammability rating: UL 94V-0
- ◆ Marking : Marking Code + Date Code
- ◆ Packaging : Tape and Reel

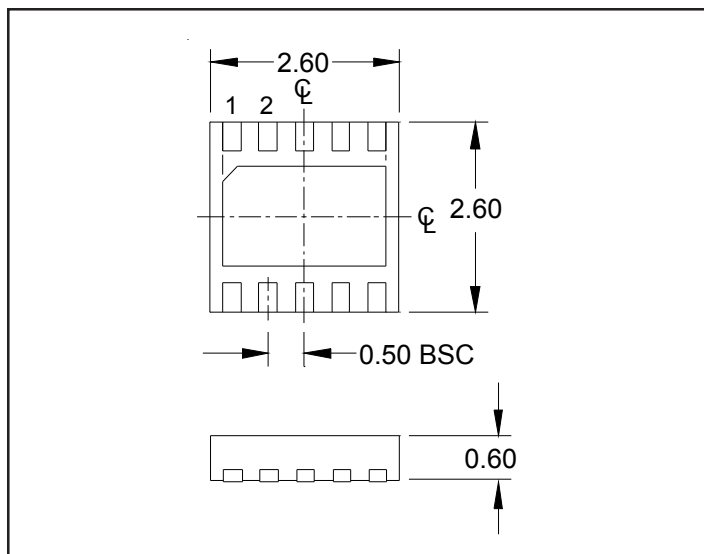
Applications

- ◆ 10/100/1000 Ethernet
- ◆ RJ-45 Connectors
- ◆ T1/E1 Secondary Protection
- ◆ T3/E3 Secondary Protection
- ◆ Analog Video

Circuit Diagram



Package Configuration



PROTECTION PRODUCTS
Absolute Maximum Rating

Rating	Symbol	Value	Units
Peak Pulse Power ($t_p = 8/20\mu s$)	P_{pk}	450	Watts
Peak Pulse Current ($t_p = 8/20\mu s$)	I_{pp}	25	A
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V_{ESD}	25 15	kV
Operating Temperature	T_J	-55 to +125	°C
Storage Temperature	T_{STG}	-55 to +150	°C

Electrical Characteristics (T=25°C)

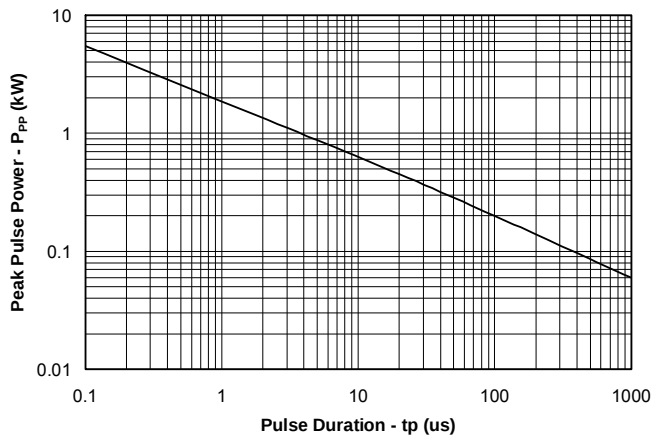
RClamp3304NA						
Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V_{RWM}				3.3	V
Punch-Through Voltage	V_{PT}	$I_{PT} = 5\mu A$	3.5			V
Snap-Back Voltage	V_{SB}	$I_{SB} = 50mA$	2.8			V
Reverse Leakage Current	I_R	$V_{RWM} = 3.3V, T=25^\circ C$			0.5	μA
Clamping Voltage	V_C	$I_{pp} = 1A, t_p = 8/20\mu s$			5.5	V
Clamping Voltage	V_C	$I_{pp} = 10A, t_p = 8/20\mu s$			10.5	V
Clamping Voltage	V_C	$I_{pp} = 25A, t_p = 8/20\mu s$			18	V
Junction Capacitance	C_j	Between I/O pins and Ground $V_R = 0V, f = 1MHz$		3.8	5	pF
		Between I/O pins $V_R = 0V, f = 1MHz$		2.0		pF

Note 1: I/O pins are pin 1, 3, 7, and 9

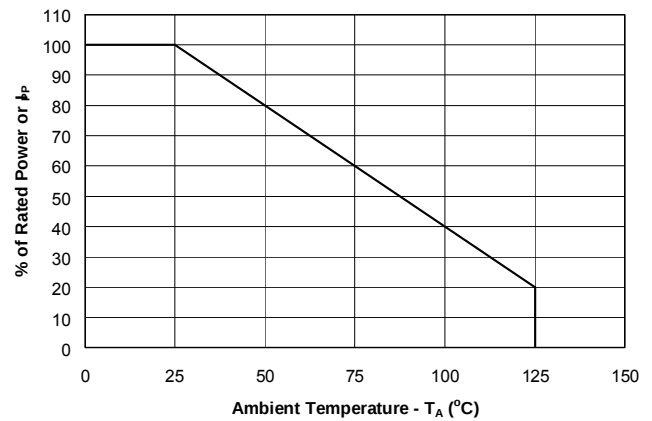
PROTECTION PRODUCTS

Typical Characteristics

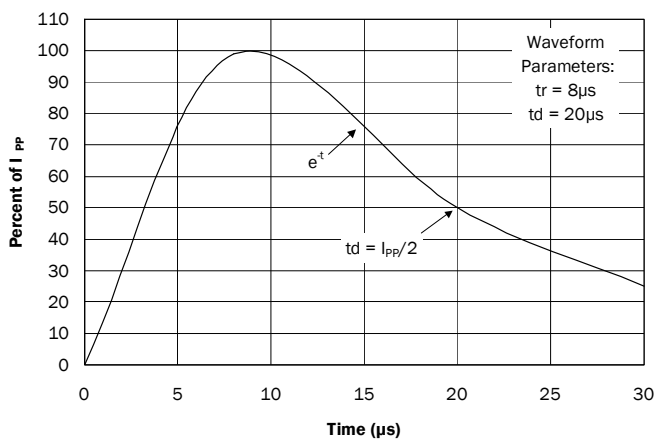
Non-Repetitive Peak Pulse Power vs. Pulse Time



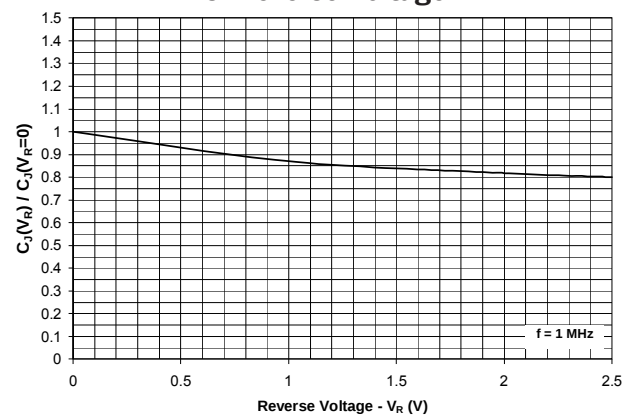
Power Derating Curve



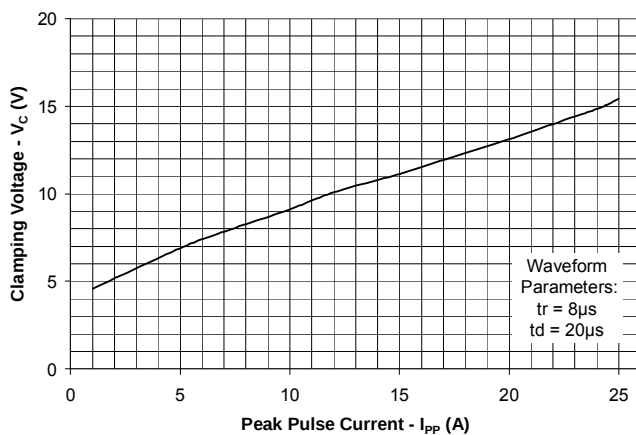
Pulse Waveform



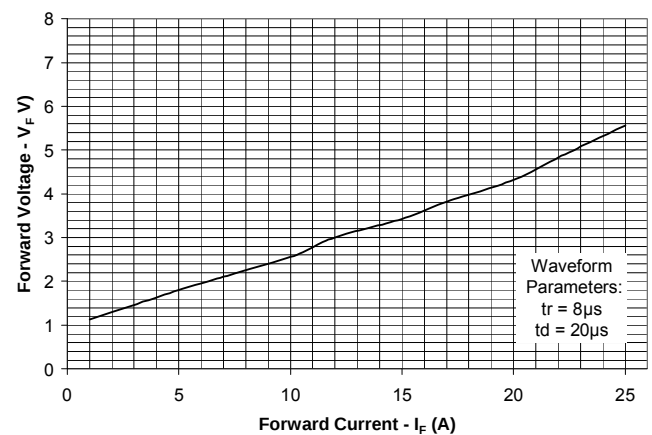
Normalized Junction Capacitance vs. Reverse Voltage

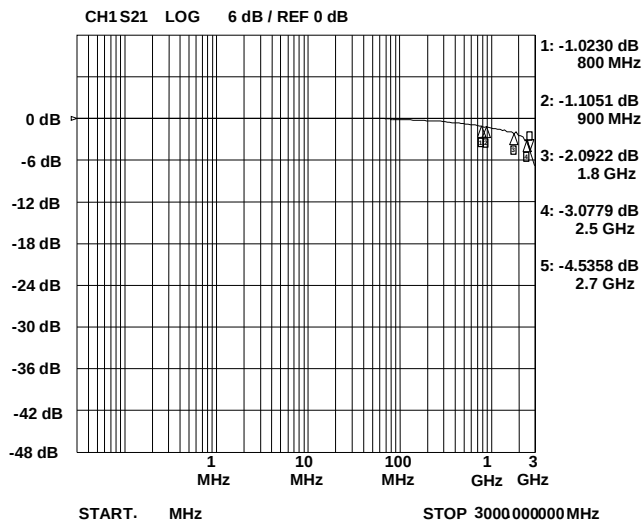
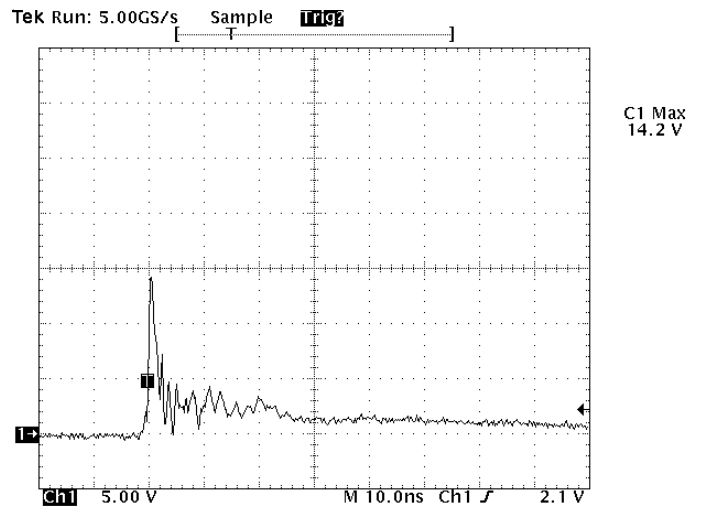


Clamping Voltage vs. Peak Pulse Current I/O to GND



Clamping Voltage vs. Peak Pulse Current I/O to I/O



PROTECTION PRODUCTS
Typical Characteristics
Insertion Loss S21 (I/O to Ground)

**ESD Clamping
(8kV Contact per IEC 61000-4-2)**


Note: Data is taken with a 10x attenuator

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Applications Information

Device Connection Options for Protection of Four High-Speed Data Lines

These devices are designed to protect low voltage data lines operating at 3.3 volts. When the voltage on the protected line exceeds the reference voltage the steering diodes are forward biased, conducting the transient current away from the sensitive circuitry. Data lines are connected at pins 1, 3, 7 and 9. The center pin should be connected directly to a ground plane. The path length is kept as short as possible to minimize parasitic inductance. Pins 2, 4, 6, 8, and 10 are not connected.

Note that pin 5 is connected internally to the cathode of the low voltage TVS. It is not recommended that these pins be directly connected to a DC source greater than the snap-back voltage (V_{SB}) as the device can latch on as described below.

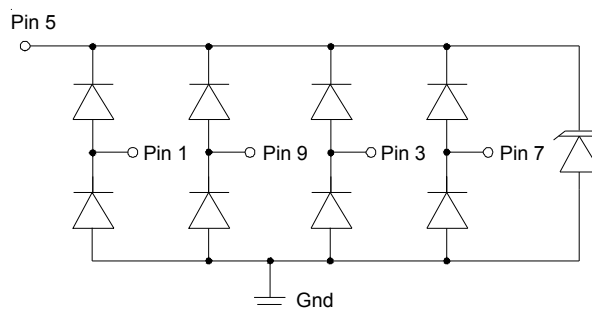
EPD TVS Characteristics

These devices are constructed using Semtech's proprietary EPD technology. By utilizing the EPD technology, the RClamp3304NA can effectively operate at 3.3V while maintaining excellent electrical characteristics.

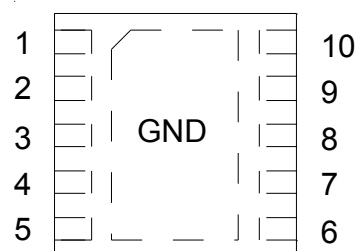
The EPD TVS employs a complex npnp structure in contrast to the pn structure normally found in traditional silicon-avalanche TVS diodes. Since the EPD TVS devices use a 4-layer structure, they exhibit a slightly different IV characteristic curve when compared to conventional devices. During normal operation, the device represents a high-impedance to the circuit up to the device working voltage (V_{RWM}). During an ESD event, the device will begin to conduct and will enter a low impedance state when the punch through voltage (V_{PT}) is exceeded. Unlike a conventional device, the low voltage TVS will exhibit a slight negative resistance characteristic as it conducts current. This characteristic aids in lowering the clamping voltage of the device, but must be considered in applications where DC voltages are present.

When the TVS is conducting current, it will exhibit a slight "snap-back" or negative resistance characteristics due to its structure. This point is defined on the curve by the snap-back voltage (V_{SB})

Circuit Diagram

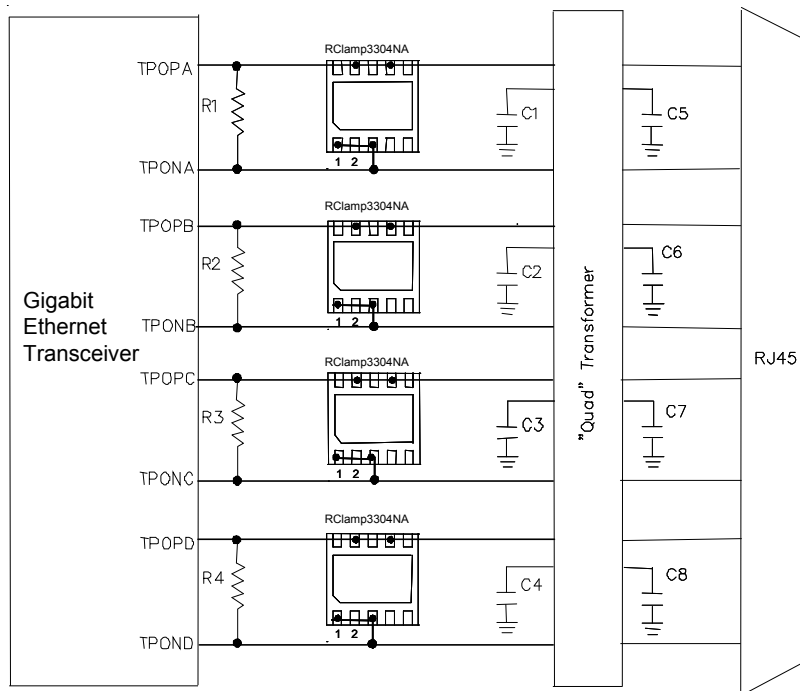


Pin Configuration (Top Side View)

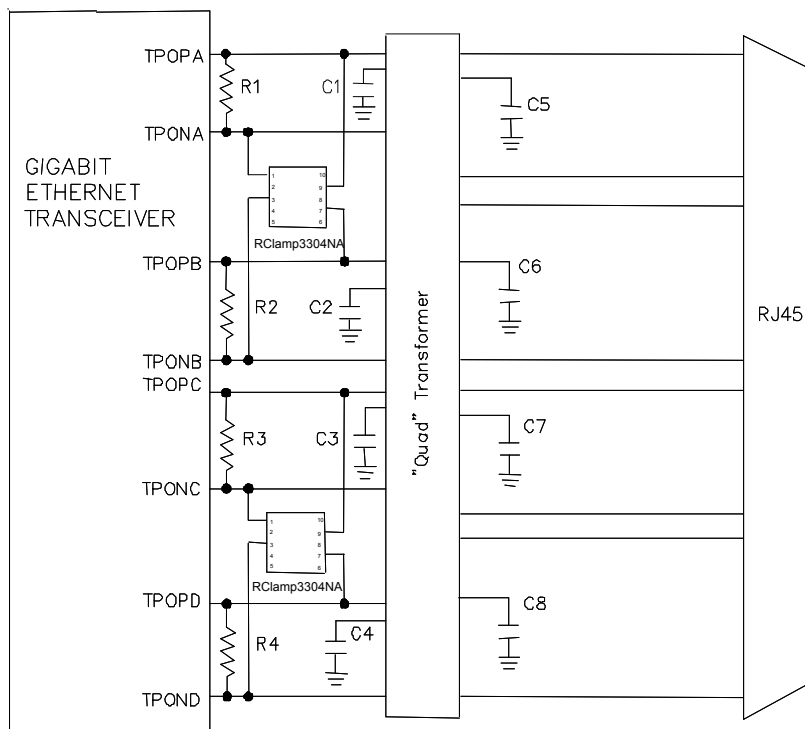


Pin	Identification
1, 3, 7, 9	Input/Output Lines
2, 4, 6, 8, 10	No Connect
5	No Connect (Do not connect this pin to a DC supply)
Center Tab	Ground

and snap-back current (I_{SB}). To return to a non-conducting state, the current through the device must fall below the I_{SB} (approximately <50mA) and the voltage must fall below the V_{SB} (normally 2.8 volts for a 3.3V device). If a 3.3V TVS is connected to 3.3V DC source, it will never fall below the snap-back voltage of 2.8V and will therefore stay in a conducting state.

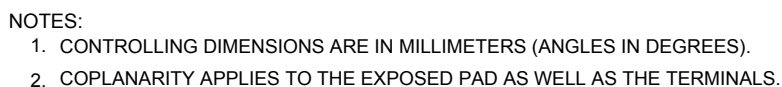
PROTECTION PRODUCTS
Applications Information


Schematic Diagram for Gigabit Ethernet Telcordia GR-1089 Intra-Building Protection



Schematic Diagram for Gigabit Ethernet ESD Protection

Outline Drawing - SLP2626P10

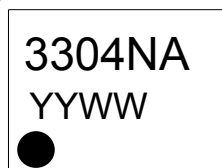


NOTES:

1. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY. CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.

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Marking



RClamp3304NA

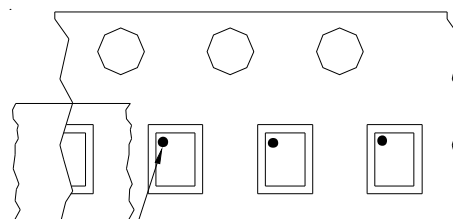
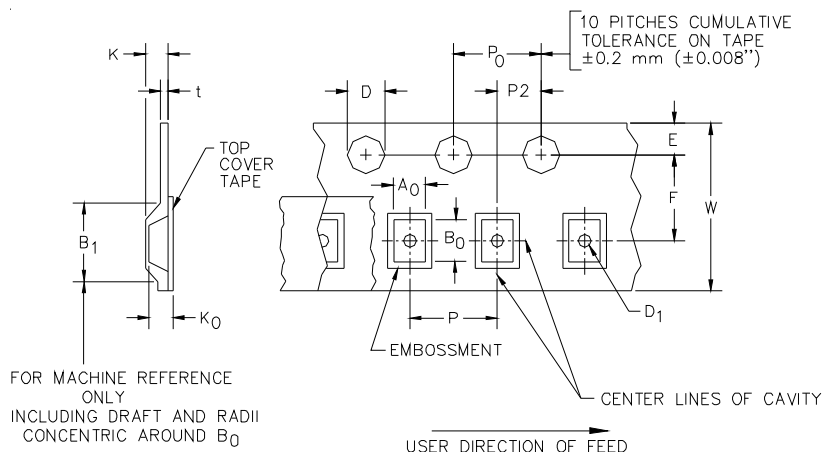
Ordering Information

Part Number	Working Voltage	Qty per Reel	Reel Size
RClamp3304NATCT	3.3 Volts	3,000	7 Inch

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YYWW = Date Code

Tape and Reel Specification



Pin 1 Location

→
User Direction of feed

Device Orientation in Tape

A0	B0	K0
2.77 +/-0.10 mm	2.77 +/-0.10 mm	0.80 +/-0.10 mm

Tape Width	B, (Max)	D	D1	E	F	K (MAX)	P	P0	P2	T(MAX)	W
8 mm	4.2 mm (.165)	1.5 + 0.1 mm - 0.0 mm	1.0 mm ±0.05	1.750±.10 mm	3.5±0.05 mm	2.4 mm	4.0±0.1 mm	4.0±0.1 mm	2.0±0.05 mm	0.4 mm	8.0 mm + 0.3 mm - 0.1 mm

Contact Information

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